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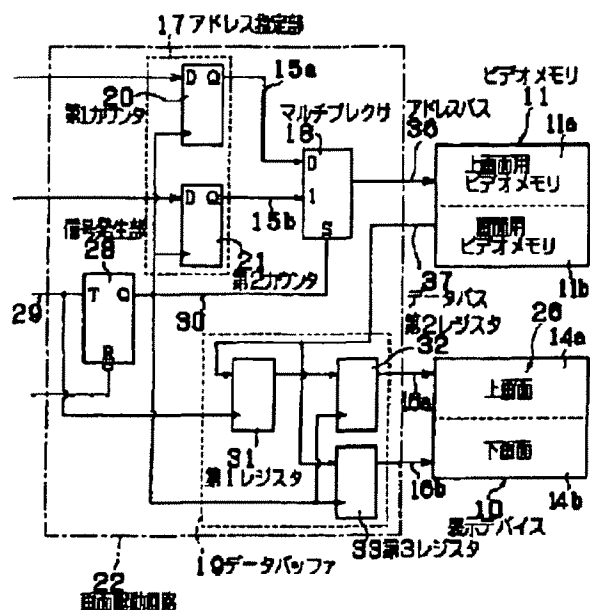
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APPLICANT : DIGITAL:KK;

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TITLE : SCREEN DRIVING CIRCUIT



ABSTRACT : **PURPOSE:** To reduce a manufacturing cost by simplifying circuit constitution required for displays and to perform displays having quick response by suppressing data processing amounts to absolute minimums in the case of diving a display screen into two screens of upper and lower screens and of scanning both screen parallelly.

CONSTITUTION: Two counters 20, 21 are provided in this circuit in accordance with upper and lower screens 14a, 14b and also both counters 20, 21 hold addresses while updating addresses of video memories 11 corresponding to scanning positions on screens. The addresses are taken out selectively by a multiplexer 18 and data are read out from video memories 11 to a data buffer 19 and also the data are transmitted to a display device 10 at a stage in which one pair of data for both upper and lower screens are completed.

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